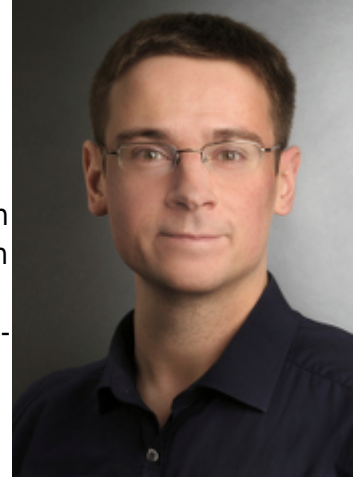


Dr. rer. nat. Sebastian Biallas

Über mich

Ich bin nach Berlin gezogen.

Am Lehrstuhl habe ich das DFG Projekt „Verifikation von SPS-Programmen mittels Model-Checking und statischer Analyse“ bearbeitet. Speicherprogrammierbare Steuerungen (SPSen) sind Steuerungscomputer zur Automatisierung, Regelung und Steuerung von großtechnischen Anlagen und Maschinen. In diesem Projekt geht es darum, mittels formaler Methoden Fehler in SPS-Programmen zu finden oder zu beweisen, dass sie ihre Spezifikation erfüllen. Insbesondere ist es dazu nötig, geeignete Abstraktionstechniken zu entwickeln, die es erlauben, Berechnungsschritte auf einer großen Menge von Werten gleichzeitig auszuführen. Erst so ist es möglich, die Analysen auf größere SPS-Programme anzuwenden.



Ich bin weiterhin unter [biallas\[at\]embedded\[dot\]rwth-aachen\[dot\]de](mailto:biallas[at]embedded[dot]rwth-aachen[dot]de) erreichbar.

Publikationen

[Bia16]

[PDF](#)[BIB](#)

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  author = { Biallas, Sebastian },
  othercontributors = { Kowalewski, Stefan and Fay, Alexander },
  title = { Verification of programmable logic controller code using
    model checking and static analysis },
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  school = { RWTH Aachen University },
  pages = { 1 Online-Ressource (x, 153 Seiten) : Illustrationen,
    Diagramme },
  series = { Aachener Informatik Berichte },
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  Discrete Systems, Cancun (Mexico), 2015-05-27 - 2015-05-29 },
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Bibtex entry :

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  author = { Beckschulze, Eva and Biallas, Sebastian and Kowalewski,
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[BBK12b]

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[BBK11]

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  author = { Biallas, Sebastian and Brauer, J{"o}rg and Kowalewski,
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